

Computer Organization and Design:  
The Hardware/Software Interface, Second Edition

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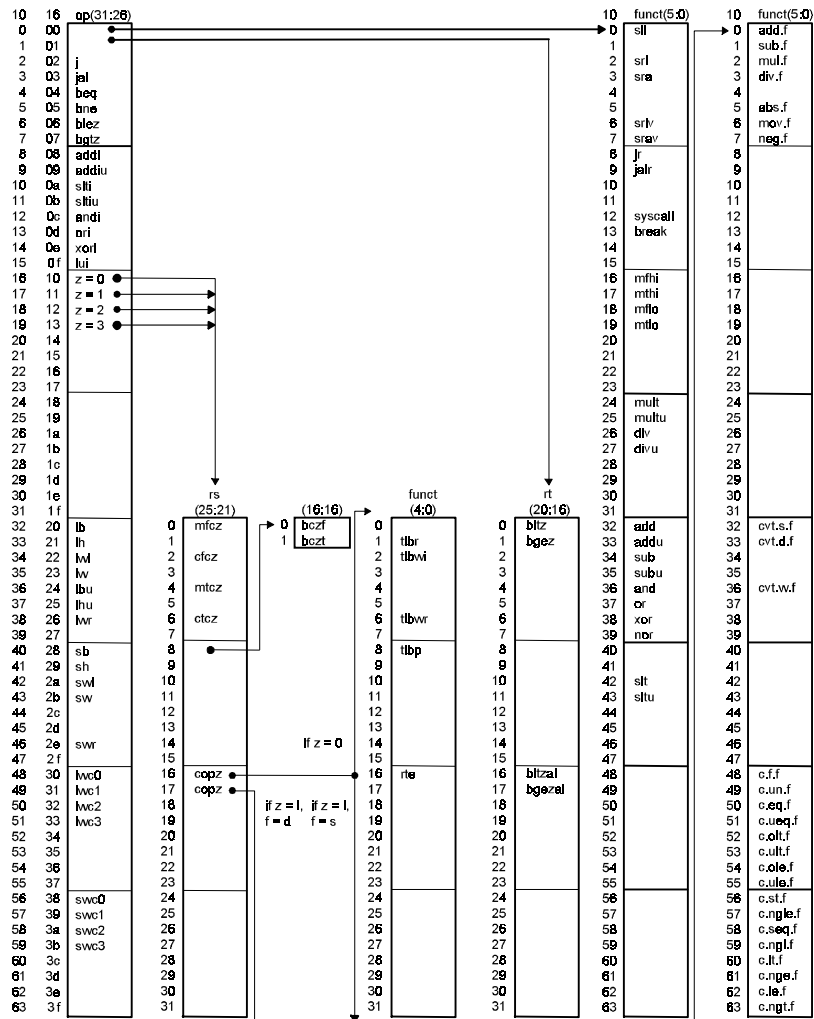
**ERRATA FOR FIRST PRINTING**

13 January, 1998

No.	Page Number	Error
1.	-	Front endsheet, right-hand side: change Chapter 9 title to "Multiprocessors"
2.	-	Copyright page, line 32: change "mkp@mkp.com" to "cod2bugs@mkp.com"
3.	xxviii	Line 17: change "mkp@mkp.com" to "cod2bugs@mkp.com"
4.	xxviii	Line 51: change "Southwest Texas University" to "Southwest Texas State University"
5.	xxix	Line 4: change "Farleigh" to "Fraleigh"
6.	47	Line 1: change "1.36" to "1.27"
7.	90	In the table in Exercise 2.2, column 3, row 1: change "160 x 16006" to "160 x 160 <sup>6</sup> "
8.	92	In the table in Exercise 2.13, column 3, row 3: change "8" to "3"
9.	113	Line 18: change "A[8] = h + A[8];" to "A[12] = h + A[8];"
10.	138	Figure 3.11: 1) move text in column 1, row 1 to column 2, row 3, 2) move text in column 2, row 1 to column 2, row 4, 3) delete empty cells in row 1.
11.	143	Line 27: change "\$s0, 4(\$sp)" to "\$s0, 0(\$sp)"
12.	144	Line 14: change "\$s0, 4(\$sp)" to "\$s0, 0(\$sp)"
13.	161	Column 3, 28 rows from the top: change "lw" to "sw"
14.	169	Line 29: change "encouters" to "encounters"
15.	199	Line 1: change "{Ex. 3.7}" to "{Ex. 3.6}"
16.	199	Line 15: change "equals" to "do not equal"
17.	199	Line 16: delete "do not"
18.	228	Figure 4.7, bottom table; column 4, row 25: change "p10000" to "10000"
19.	274	Figure 4.43, bottom table; column 4, row 3: change "\$s1 = \$s2 + \$s3" to "\$s1 = \$s2 + 100"
20.	329	Exercise 4.41, line 6: change "two instructions" to "one instruction"
21.	330	Exercise 4.44, line 1: change "on page 242" to "at the top of page 247"

- 22. **333** *Exercise 4.53, line 4:* change "multiplicand" to "product"
- 23. **373** *Line 4:* insert "2" before "nanoseconds"
- 24. **381** *Figure 5.32, line 1 of caption:* change "ALUSelB" to "ALUSrcB"
- 25. **488** *Line 9:* change "Exercise 6.17" to "Exercise 6.20"
- 26. **488** *Line 16:* change "ALUSelB" to "ALUSrcB"
- 27. **502** *Beneath the "Elaboration":* Insert the following *additional* Elaboration: A side effect of moving branch decision to the decode pipeline stage is that we have to copy the muxes and control for forwarding and stalling to the decode stage too. That is, an add or load followed by a branch that tests the result of the instruction must still work correctly and efficiently even if we change the pipeline.
- 28. **535** *Line 39:* change "page 485" to "page 513"
- 29. **564** *Line 25:* change "sycles" to "cycles"
- 30. **A-22** *Line 28:* change "first free" to "last"
- 31. **A-23** *Figure A.10, column 1, row 24:* change "\$t7" to "\$s7"
- 32. **A-26** *Line 5:* change "subtracting the frame size from" to "adding the frame size to"
- 33. **A-31** *Line 2:* change "latter" to "later"
- 34. **A-31** *Line 22:* Insert the following line between line 22 and line 23: "addu \$v0, \$v0, 1"

35. A-54 Figure A.19: replace original figure with the following:



36. **A-63** *Line 14 (Branch on not equal):* change "bne rs, label" to "bne rs, rt, label"
37. **A-65** *Line 8 (Jump and link):* change "register rd" to "register ra (register 31)"
38. - *Back endsheet, left-hand side; bottom table, column 4, row 3:* change "\$s1 = \$s2 + \$s3" to "\$s1 = \$s2 + 100"